



Docket No.: 1572.1252

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Se-Young JANG et al

Serial No. 10/822,669

Group Art Unit: 2814

Confirmation No. 4799

Filed: April 13, 2004

Examiner: DOAN, Theresa T.

For: METHOD OF SURFACE-MOUNTING SEMICONDUCTOR CHIP ON PCB

AMENDMENT AND REQUEST FOR EXAMINER INTERVIEW

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed June 28, 2006, and having a period for response set to expire on September 28, 2006. A petition and fee for a two-month Extension of Time is enclosed, thereby extending the response period to November 28, 2006.

Reconsideration of the claims is respectfully requested. The following remarks are respectfully submitted.